

SIDDHARTH INSTITUTE OF ENGINEERING AND TECHNOLOGY
SURYA VIHAR, EKTAGUDA, KORAPUT

TIME TABLE-5TH SEMESTER ELECTRICAL ENGG.

DAY	9:30 to 10:25	10:25 TO 11:20	11:20 TO 12:15	12:15 TO 1:10	01:10 TO 01:45	1:45 TO 2:40	2:40 TO 3:35	3:35 TO 4:30
MONDAY	DE & M ✓	UEET	EM & ST	PE & PLC	L U N C H			ELECTRICAL MECHINE LAB
TUESDAY	DE & M ✓	UEET	EM & ST	EC-II				ELECTRICAL MECHINE LAB
WEDNESDAY	UEET	DE & M ✓	EM & ST	PE & PLC				POWER ELECTRONICS & PLC LAB
THURSDAY	EC-II	DE & M ✓	EM & ST	PE & PLC				DIGITAL ELECTRONIC LAB
FRIDAY	EC-II	UEET	DE & M ✓	PE & PLC				PROJECT PH -1
SATURDAY	DE & M	EC-II	UEET	PE & PLC				SCA

TH-1 - E M & ST - ENTREPRENEURSHIP MANAGEMENT & SMART TECHNOLOGY

TH-2 - E C-II - ENERGY CONVERSION-II

TH-3 - DEM - DIGITAL ELECTRONICS & MICROPROCESSOR

TH-4 - UEE & T - UTILIZATION OF ELECTRICAL ENERGY TRACTION

TH-5 - PE & PLC -POWER ELECTRONICS & PLC

PR-1- ELECTRICAL MACHINE LAB-II

PR-2- POWER ETC & PLC LAB

PR-3- DIGITAL ETC & MICROPROCESSOR LAB

PR-4- PROJECT PHASE-I

K. J. Parida
HOD
Electrical Engg
SIET, KORAPUT
12/7/2025

K. J. Parida
PRINCIPAL
Principal
Siddharth Institute of Engineering & Technology
Koraput
12/07/2025